

ADSP - 2181 Based DSP Board

The evaluation board is designed to be used in conjunction with VisualDSP and the 16-bit tools as a complete code evaluation and debug system. Using the SAR-ADSP-2181 with the debugger, you can observe the ADSP-2181 execute programs from on-chip RAM, interact with on-board devices, and communicate with other peripherals located on optional add-on modules.

You can access the ADSP-2181 processor from the PC through a serial port or an optional emulator. The monitor program gives you complete target debug capability through the serial port. In contrast, the emulator allows the PC to perform in-circuit emulation through the processor's emulation port.

Key Features :

- > ADSP-2181, 33 MIPS DSP
- > AD1819B, AC97 stereo codec
- > RS-232 interface
- > Socketed EPROM
- > User push buttons
- > Power supply regulation
- > Expansion connectors
- > User configurable jumper

Additionally, the SAR-ADSP-2181 provides user installed expansion connectors that let you examine processor signals, as well as provide an interface for host control.

This product is used with the VisualDSP integrated development environment (IDE) and debuggers which contains the code generation tools (C compiler, linker, and assembler). VisualDSP is a Windows-based software development environment for Analog Devices Digital Signal Processors (DSP). The VisualDSP software aids your DSP software development efforts by providing you with the following:

Experiments :

- > Amplification of input wave file using codec.
- > Generation of DTMF dial tone.
- > Generation of DTMF tone for a particular digit.
- > Generation of ambulance tone.
- > FIR filter program for various cutoff frequencies.



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ADSP - BF537 Blackfin Processor

The ADSP BF537 EZ KIT Lite provides developers with a cost effective method for initial evaluation of the ADSP Bf537 blackfin Processor for a wide range of applications including audio and video processing. The EZ KIT Lite includes an ADSP Bf537 desktop evaluation board and fundamental debugging software to facilitate architecture evaluations via a USB based PC hosted tool set. Real time debugging is made possible via the Background Telemetry Channel (BTC) feature. Through BTC, data can be streamed both to and from the processor over the JTAG connection between host and embedded processor without the overhead involved with halting the target application getting the desired information, and then restarting the target application. With this EZ KIT Lite, users can learn more about ADI's ADSP Bf537 hardware and software development and prototype applications. The ADSP Bf537 EZ KIT Lite provides an evaluation suite of the Visual DSP++ integrated development and debugging environment with C/C++ compiler, advanced plotting tools, statistical profiling, Visual DSP++ Component Software Engineering (VCSE), and the VisualDSP++ Kernel (VDK). Other features of VisualDSP++ include; assembler, linker, libraries, and splitter. VisualDSP++ offers programmers a powerful programming tool with flexibility that significantly reduces the time-to-market. The VisualDSP++ software included with the EZ KIT Lite is limited in program memory size for use solely with the EZ KIT Lite product.



Specifications

- > ADSP-BF537 Blackfin® Processor
- > 32 MB (16M x 16-bit) SDRAM
- > 2 MB (512K x 16-bit x 2) FLASH memory
- > AD1836 96 kHz audio codec w/ 4 input and 6 output RCAjacks
- > ADV7183 video decoder w/ 3 input RCA jacks
- > ADV7171 video encoder w/ 3 output RCA jacks
- > ADM3202 RS-232 line driver / receiver
- > DB9 male connector
- > USB based debugger interface
- > JTAG ICE 14-Pin header
- > SPORT0 connector
- > Background Telemetry Channel
- > Evaluation suite of VisualDSP++
- > 10 LEDs : 1 Power, 1 Board Reset, 1 USB Reset, 1 USB Monitor, 6 General Purpose
- > 5 Push Butons w/ debounce logic: 1 Reset, 4 Programming Flag
- > Three 90-Pin connectors providing PPI, SPI, EBIU Timers0-2, UART, Programmable Flags, SPORT0, and SPORT1 expansion interfaces for analyzing and interfacing
- > With user manuals .

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