

Advance Digital Experimental System

Technical Specifications :-

- > The system should be based around CPLD with a graphical LCD display
- > 28 pin TTL in and TTL out emulations.
- > Functional control keypads.
- > On Board signal generator from 1Hz to 1 Mhz with 100 ms pulse.
- > Solderless IC devices for experimentation
- > 16 LED output display
- > On Board Buzzer
- > On Board 555, monostable
- > Facility for pin measurement points for various test points measurement
- > Various list of Digital Experiments must be performed
- > IC Tester circuit
- > On Board Bread board
- > On Board relay section
- > 7-segment section



Experimental Contents:-

- > Basic Logic Gates Experiment
- > Assemble Logic Gates Experiment
- > Adder Experiment
- > Subtractor Experiment
- > Assembled Logic Application
- > Digital Logical Application
- > Sequential Logic Experiment
- > Sequential Logical Application

objective:-

To help Students learn the latest tools for understanding Digital Electronic Experiments with CPLD based System

Salicon Nano Technology Pvt. Ltd.

111, 1st Floor, Laxmi Deep Tower, Laxmi Nagar District Center, Delhi - 110092, INDIA,
Tel: 91-11-22525940, 40618940; Fax; 91-11-22525941; E-mail: info@salicontech.com; Web: www.salicontech.com