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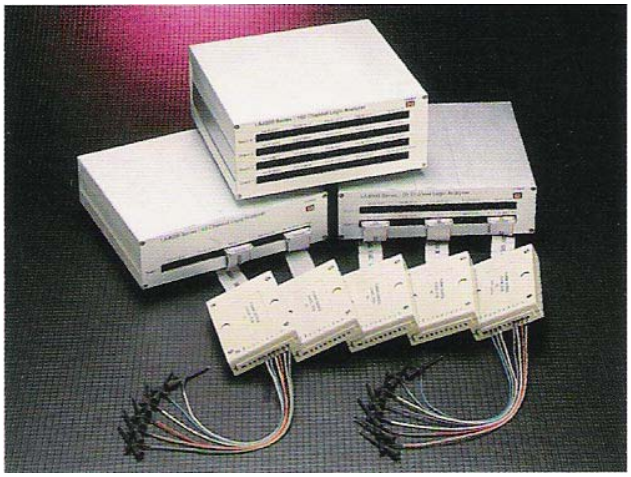
LA-2132X
Logic Analyzer with Pattern Generator

Model	LA-2132K (250 Mega)	LA-2132M (500 Mega)	LA-2132G (1Giga)
Frequency	250 MHz	500 MHz 250 MHz	1 GHz 500 MHz 250 MHz
Channel	32 Ch	16 Ch 32 Ch	08 Ch 16 Ch 32 Ch
Memory	256K	1 Mega 512K	4 Mega 2 Mega 1 Mega
Impedance	250KW < 2P	250KW < 2P	250KW < 2P
Threshold Voltage	1 SETS -3.7 to 1.9V	1 SETS -3.7 to 1.9V	1 SETS -3.7 to 1.9V
External Clock	1 Ch 0~125MHz	1 Ch 0~125MHz	1 Ch 0~125MHz
Trigger	512 Levels 32 Ch Parallel, Serial, I2C...	512 Levels 32 Ch Parallel, Serial, I2C...	512 Levels 32 Ch Parallel, Serial, I2C...
Max Input Voltage	±110V	±110V	±110V
Max Input Bandwidth	125MHz	125MHz	125MHz
PC Interface	USB only (USB 1.1/ 2.0)	USB only (USB 1.1/ 2.0)	USB only (USB 1.1/ 2.0)

LA-5580
40/80 Channel Logic Analyzer



FEATURES

1. High speed operation (up to 500M samples per second)
2. Up to 80 data input channels
3. Deep data buffers (up to 512K samples per channel)
4. Continuously variable pre/post trigger position
5. 200KW/<5pF high impedance probes minimize interference with test circuit
6. Variable threshold voltage
7. Multiple threshold selections allow different logic types to be worked with t the same time, such as TTL, ECL. 3V logic, CMOS, and RS-232
8. External trigger output for triggering other instruments, such as oscilloscopes
9. Statelist display, timing display, and mixed mode display
10. Very high data bandwidth up to 100MHz
11. 8 external high speed clock inputs with user definable combinations for flexible clock qualifying
12. Captures both state and timing simultaneously with one probe
13. Optional very high speed pattern generator with rates up to 100M points/s from 16 to 80

SPECIFICATIONS

1. External clock	: 8 channels 0-80MHz
2. Frequency	: 500Mhz 48 channels 1Hz ~ 250MHz 16 channels
3. Memory	: 512K/48 channels 256K/80 channels
4. Maximum channels	: 80
5. Trigger level	: 16 levels 80 channels complex
6. Trigger quality	: 8 channels
7. Pattern Generator (Optional)	: 0 ~ 50MHz (0 ~ 100MHz rate
8. Threshold voltage	: 6 sets 6.4V ~ +6.4V
9. Input bandwidth	:100MHz
10. Input impedance	: 200KW/5pF
11. Maximum input voltage	: +/-150V
12. Power supply	: DC Adapter 5V/10A
13. PC Interface	: Parallel port/USB 1.1/2.0
14. Accessories	: Logic pod, USB 2.0 adapter, parallel cable (IEEE 1284), colored wires and dips, DC adapter, user manual and CD-ROM.

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LA-3000
32 Channel Standalone Logic Analyzer

SPECIFICATIONS

Input

1. Input Channel : 32 data-sampling channels, 1external clock channel, 3 clock restriction channels
2. Threshold level : 15 individually adjustable threshold levels with the adjusting range of 10V~+10V and the resolution of 0.1V
3. Input impedance : >100KW
4. Input protection : has the function of overvoltage protection and can endure the voltage impact of ±100V

Sampling Storage

1. Sampling clock : internal, external, rise edge, fall edge
2. Clock restriction : without restriction, 3-channel restriction with the setting of 0, 1, arbitrary for each channel
3. Sampling cycle : 10ns~10s (rate 0.1~100MSa/s)
4. Resolution : 10ns
5. Sampling signal source : internal, external
6. Storage depth : 32k sampled points for each channel and 117 screens of the movable length
5. Storage mode : continuous storage, interval storage (clock restriction)
6. Storage start-up : manual, conditional
7. Start-up condition : 8 channels with 0, 1 or arbitrary for each channel

Triggering

1. Triggering condition : 4 groups and 8 channels for each group with 0, 1 or arbitrary for each channel
2. Condition combination : each group can be independent, or logical combination of AND, OR, or unconditioned for the four groups.
3. Effective triggering time : 1-255 times
4. Delay length after triggering : 1-32767 sampling cycles

Display

1. Screen : 5.7" LCD, TFT,
2. Resolution : 320x240 points
3. Display format : 8 channels of timing waveforms, 16 channels of timing waveforms, 32 channels of state data
4. Graph zoom : horizontal zoom coefficient: 1-255 times
5. Horizontal moving : horizontal rate: 1-999 points/ step and 117 screens of the movable length

Cursor

1. Benchmark cursor : any point in timing waveform can be defined as the benchmark cursor and the data can be displayed
2. Measuring cursor : the cursor position is movable, and the data as well as the time difference of the benchmark cursor can be displayed dynamically
3. Triggering cursor : sampling points according with the triggering condition in timing waveform or state data

Simulation

1. Internal signal waveform : four types of waveforms: counter, sequencer, pulse switch, cumulater
2. Internal signal cycle : 20ns-10s
3. Resolution : 10ns

Operation

Keyboard operation, convenient with English menu and sequence adjusting with knob

Power Supply

1. Voltage : AC220 (1±10%) V;
2. Frequency : 50 (1±5%) Hz
3. Power : <30VA

Environment

1. Temperature : 0~40°C
2. Humidity : <80%

Physical

1. Dimensions : 329mm x 283 mm x 155mm
2. Weight : 3Kg
3. Manufacturing technics : surface conjoint technics, large scale integrated circuit, high reliability, long service life

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