

Universal FPGA / CPLD Protoboard

Technical Specifications

- > Universal Platform for FPGA & CPLD devices [XCV50-4PQ240C) (With compatibility for devices upto 800K gates)
- > Operating Frequency minimum 100 Mhz.
- > 4X4 matrix keyboard.
- > 16 digital I/Ps and O/Ps with LED indication
- > Interface to RS232 with 9- pin D-type connector.
- > 4 X 7 Segment Multiplexed displays
- > Support for Multilinx cable interface
- > All configuration modes supported by Xilinx to be provided.
- > On board 40 MHz Clock with compatibility for clocks upto 300 Mhz.
- > 16 Channel Interface to Standard Logic Analysers through MICTOR Connector

Features

- > Supports devices with 5V compatible I/O viz.
 - FPGAs -Virtex, Spartan-II.
 - CPLDs - CoolRunner and XC9500 series.
- > 16 digital inputs and outputs with LED indication.
- > 4X7 Segment LED displays and 4X4 Matrix Keyboard.
- > Onboard- 4MHz Clock Oscillator and Reset switch.
- > Onboard regulators to generate all required voltages.
- > RS232 interface hardware with 9 pin D type connector.
- > Configuration modes supported
 - JTAG- with onboard hardware.
 - Multilinx cable - connector interface.
 - FLASHROM - facility to mount flash prom available.
- > Available I/O's accessible to the user on 0.1" headers..
- > Maximum 102 user I/Os (actual number depends upon the device and package used).
- > Is compatible with adaptor models MXUK-144-002, MXUK-208-001 and MXUK-240-001.
- > Device Support (available on separate adapters)

SeriesDevice

- > CPLD : XC9536, XC9572, XC95108, XC95216 ,XC95288, XC95144XL, XC95288XL.
- > COOLRUNNER XPLA : XCR2C64, XCR3064XL, XCR3128XL, XCR3256XL, XCR3384XL, XCR3512XL.
- > SPARTAN II : XC2S15, XC2S30, XC2S50, XC2S100, XC2S150, XC2S200, **XC2S50E**.
- > VIRTEX : XCV50, XCV100, XCV150, XCV200, XCV300, XCV400, XCV600, XCV800,



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