

GPS Development System

Technical Specifications :-

- > Falcom JP13, 20 channel GPS Receiver based on SiRF GSC3 chip
- > ARM 7 baseband CPU with 8Mbits FLASH memory
- > 200.000+ effective correlators and Integrated TCXO
- > Protocol support for NMEA & SiFR
- > Position accuracy < 10m (without DGPS)
- > Extremely fast TTFF at low signal levels
- > GSW3 software support and SiRFLoc multi mode GPS support
- > Position accuracy < 10m (without DGPS)
- > P89C51RD2/P89C668 microcontroller prototyping module.
- > 64KB on-chip FLASH. 512 KB external serial I2C-EEPROM.
- > RS232 serial interface. 16 x 2 lines LCD Display.
- > PS2 Keyboard connector. Ethernet module (Realtek RTL8019AS, 10BaseT, RJ45) with Ethernet and standard modem drivers.
- > RTL8019AS Ethernet controller.
- > Ethernet II & IEEE802.3 10Base5, 10Base2, 10BaseT Compliant.



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